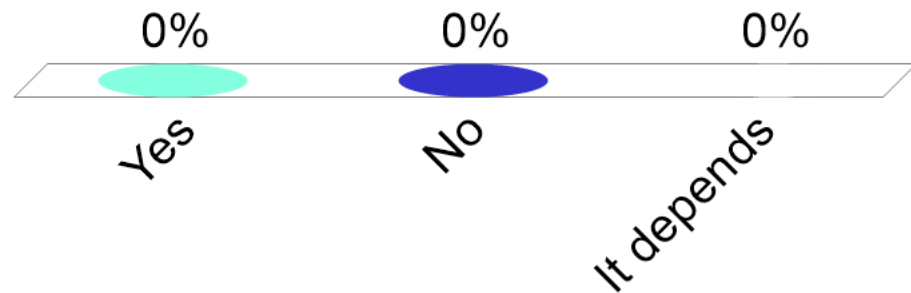


# Q1: Can we call hybrid printed electronics an OLED component integrating sputtered or evaporated Ba or Ca cathode ?

**A. Yes**

**B. No**

**C. It depends**



## Q2: What is a typical size for contact pads on a CMOS chip?

- A. 500  $\mu\text{m}$  to 1 mm
- B. 250-500  $\mu\text{m}$
- C. 100-250  $\mu\text{m}$
- D. 25-100  $\mu\text{m}$**



### Q3: Which of the following methods are adequate to interconnect electrically silicon chips to a PET foil PCB ?

**A. Isotropic conductive adhesive**

**B. Soldering**

**C. Inkjet printing**

**D. Wire bonding**

**E. Anisotropic conductive**

